

FIG. 1

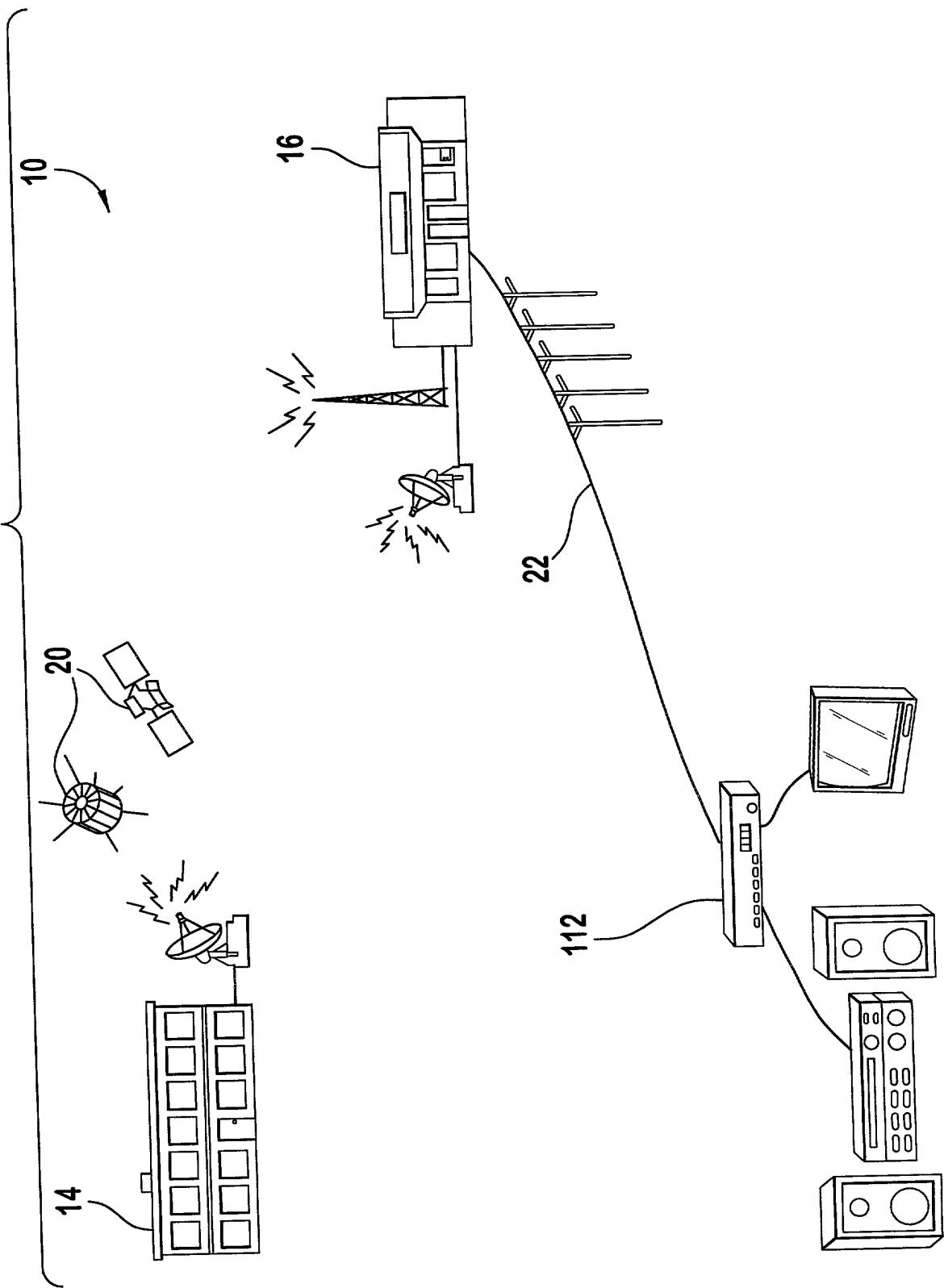


FIG. 2

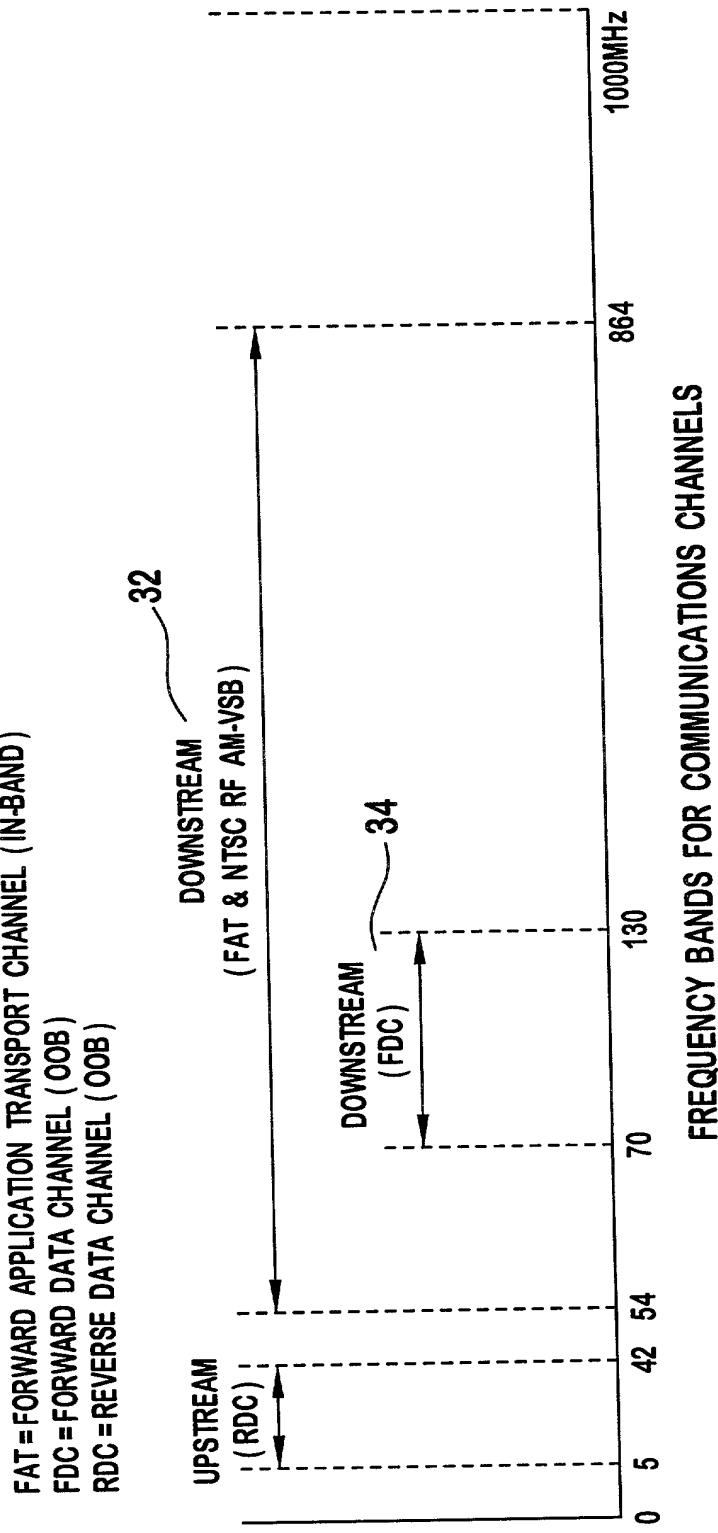
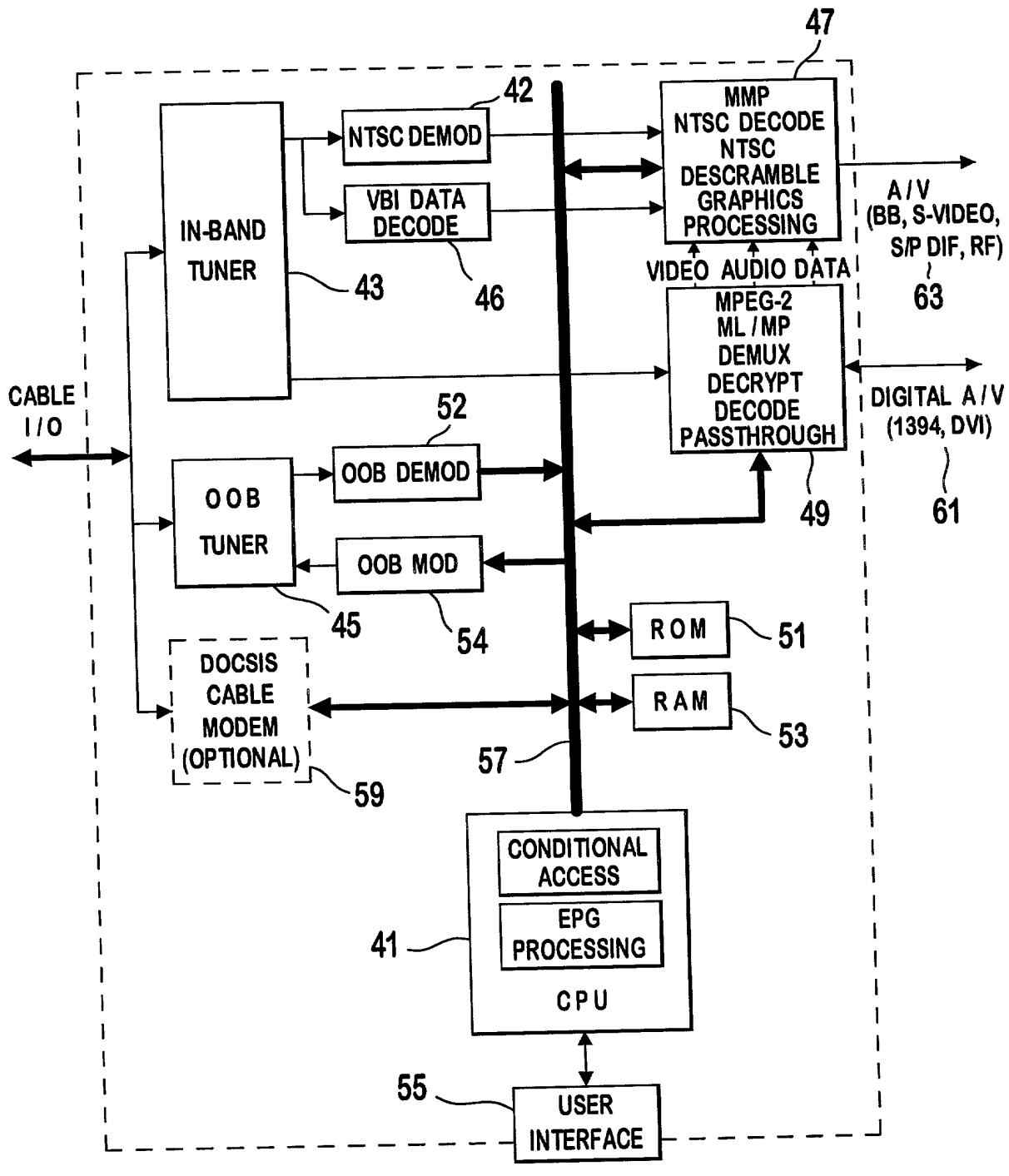


FIG. 3



GENERIC DIGITAL SETTOP TERMINAL BLOCK DIAGRAM

FIG. 4

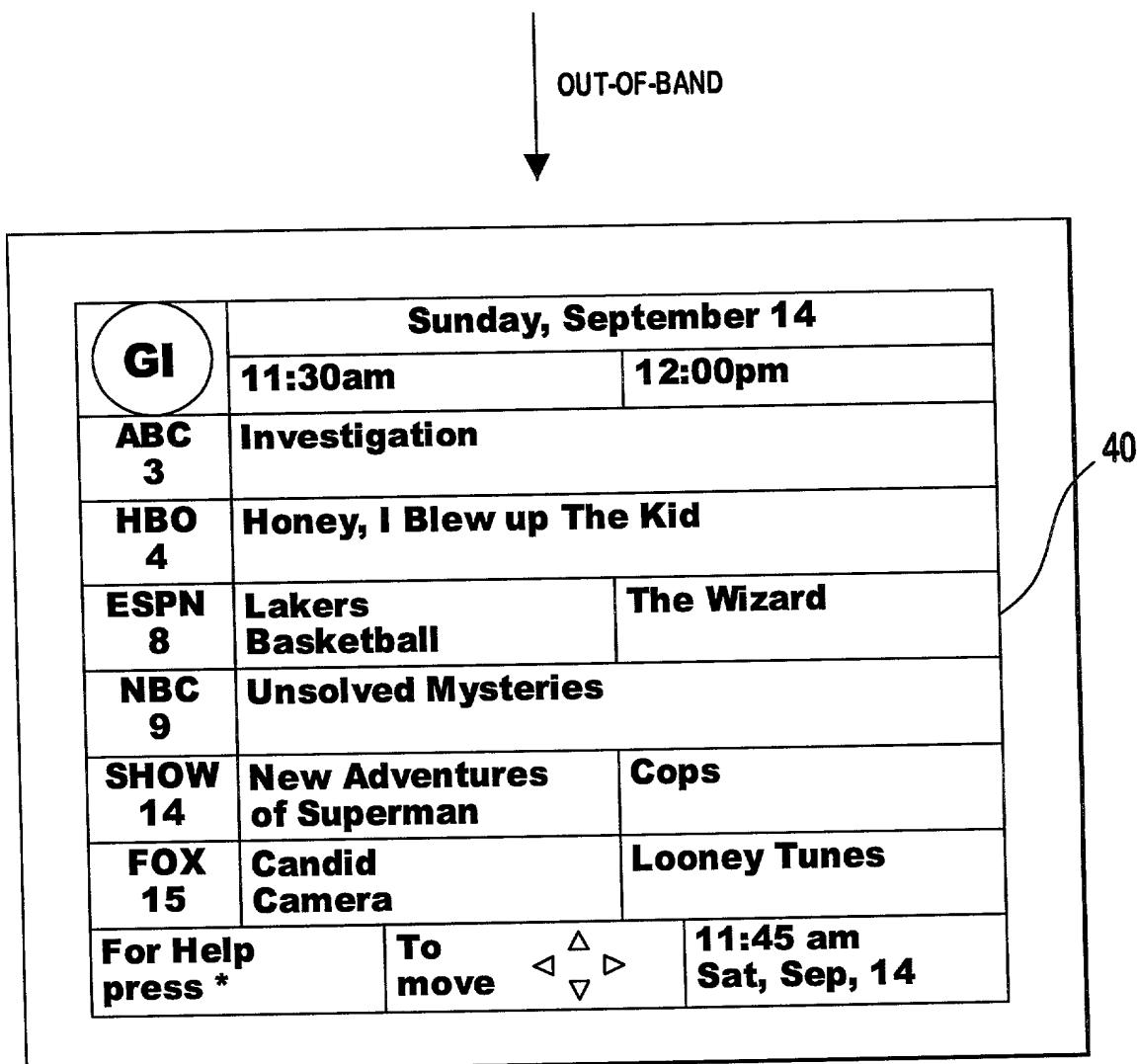


FIG. 5

